



Pin Definition:
 1. Drain 6. Drain
 2. Drain 5, Drain
 3. Gate 4. Source

PRODUCT SUMMARY

V _{DS} (V)	R _{DS(on)} (mΩ)	I _D (A)
30	30 @ V _{GS} = 10V	6.7
	42 @ V _{GS} = 4.5V	5.7

Features

- Advance Trench Process Technology
- High Density Cell Design for Ultra Low On-resistance

Application

- Load Switch
- PA Switch

Ordering Information

Part No.	Package	Packing
TSM3424CX6 RF	SOT-26	3Kpcs / 7" Reel
TSM3424CX6 RFG	SOT-26	3Kpcs / 7" Reel

Note: "G" denote for Halogen Free Product

Absolute Maximum Rating (Ta = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current	I _D	6.7	A
Pulsed Drain Current	I _{DM}	30	A
Continuous Source Current (Diode Conduction) ^{a,b}	I _S	1.7	A
Maximum Power Dissipation	P _D	Ta = 25°C	2.0
		Ta = 75°C	1.3
Operating Junction Temperature	T _J	+150	°C
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

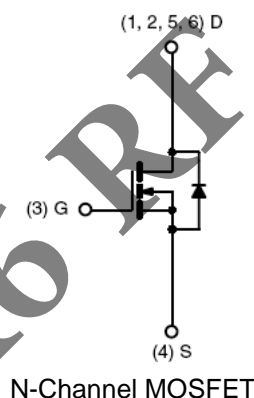
Thermal Performance

Parameter	Symbol	Limit	Unit
Junction to Case Thermal Resistance	R _{θJC}	30	°C/W
Junction to Ambient Thermal Resistance (PCB mounted)	R _{θJA}	80	°C/W

Notes:

- a. Pulse width limited by the Maximum junction temperature
- b. Surface Mounted on FR4 Board, t ≤ 10 sec.

Block Diagram

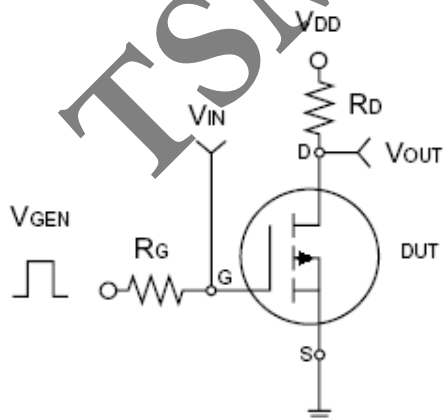


Electrical Specifications (Ta = 25°C unless otherwise noted)

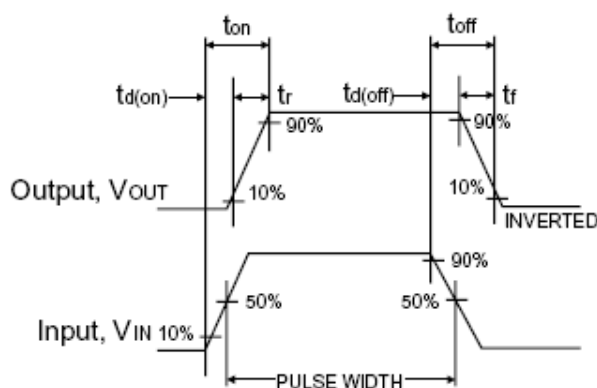
Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Static^a						
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	BV_{DSS}	30	--	--	V
Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	$V_{GS(TH)}$	1	1.4	3	V
Gate Body Leakage	$V_{GS} = \pm 20V, V_{DS} = 0V$	I_{GSS}	--	--	± 100	μA
Zero Gate Voltage Drain Current	$V_{DS} = 24V, V_{GS} = 0V$	I_{DSS}	--	--	1.0	μA
On-State Drain Current	$V_{DS} \geq 5V, V_{GS} = 10V$	$I_{D(ON)}$	30	--	--	A
Drain-Source On-State Resistance	$V_{GS} = 10V, I_D = 6.7A$	$R_{DS(ON)}$	--	23	30	m Ω
	$V_{GS} = 4.5V, I_D = 5.7A$		--	32	42	
Forward Transconductance	$V_{DS} = 5V, I_D = 5.0A$	g_{fs}	--	14	--	S
Diode Forward Voltage	$I_S = 1.7A, V_{GS} = 0V$	V_{SD}	--	0.76	1	V
Dynamic^b						
Total Gate Charge	$V_{DS} = 15V, I_D = 6.7A, V_{GS} = 10V$	Q_g	--	4.52	--	nC
Gate-Source Charge		Q_{gs}	--	1.24	--	
Gate-Drain Charge		Q_{gd}	--	1.68	--	
Input Capacitance	$V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz$	C_{iss}	--	400.96	--	pF
Output Capacitance		C_{oss}	--	100.47	--	
Reverse Transfer Capacitance		C_{rss}	--	71.82	--	
Switching^c						
Turn-On Delay Time	$V_{DD} = 15V, R_L = 2.2\Omega, I_D = 1A, V_{GEN} = 10V, R_G = 6\Omega$	$t_{d(on)}$	--	7.42	--	nS
Turn-On Rise Time		t_r	--	3.41	--	
Turn-Off Delay Time		$t_{d(off)}$	--	20.4	--	
Turn-Off Fall Time		t_f	--	3.01	--	

Notes:

- a. pulse test: PW $\leq 300\mu S$, duty cycle $\leq 2\%$
- b. For DESIGN AID ONLY, not subject to production testing.
- c. Switching time is essentially independent of operating temperature.



Switching Test Circuit

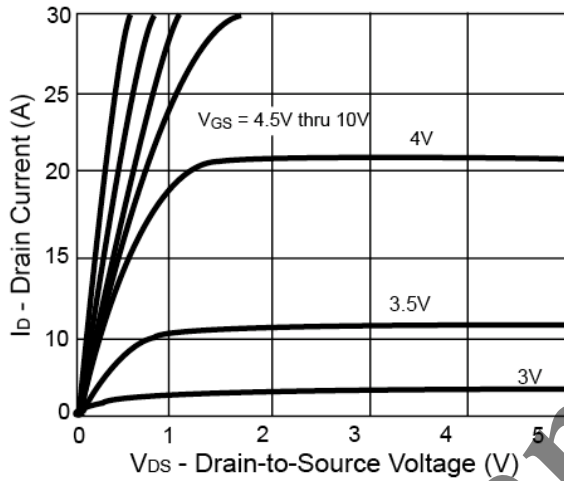


Switchin Waveforms

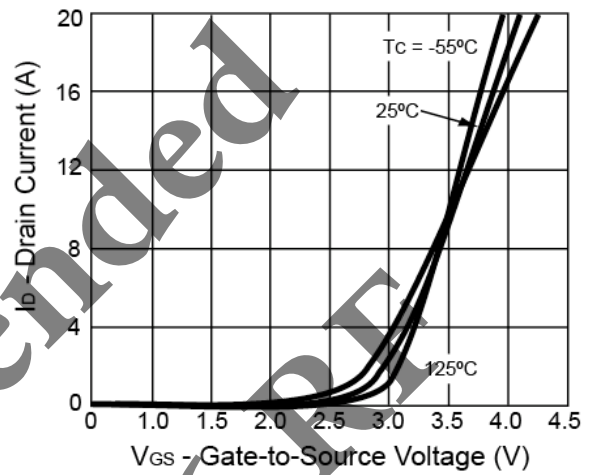


Electrical Characteristics Curve (Ta = 25°C, unless otherwise noted)

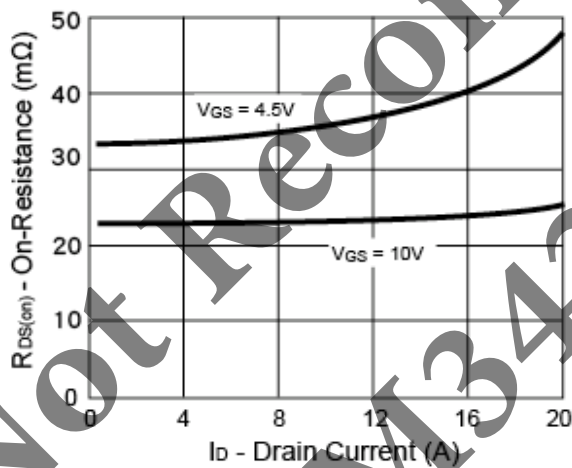
Output Characteristics



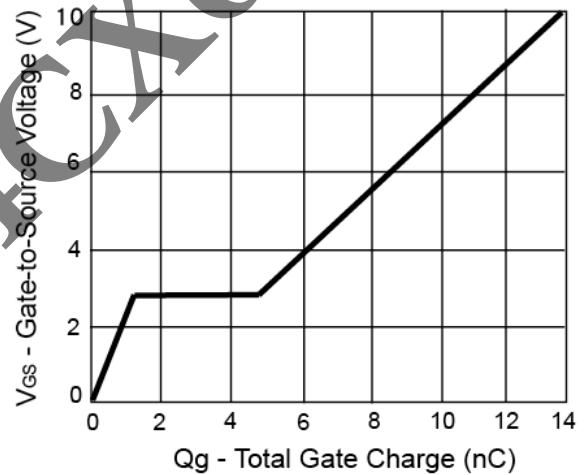
Transfer Characteristics



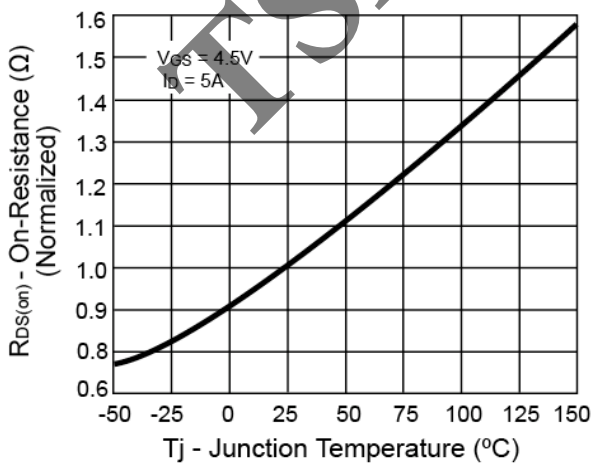
On-Resistance vs. Drain Current



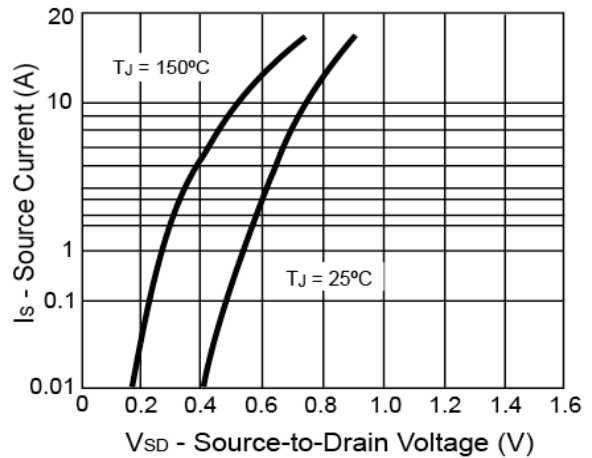
Gate Charge



On-Resistance vs. Junction Temperature



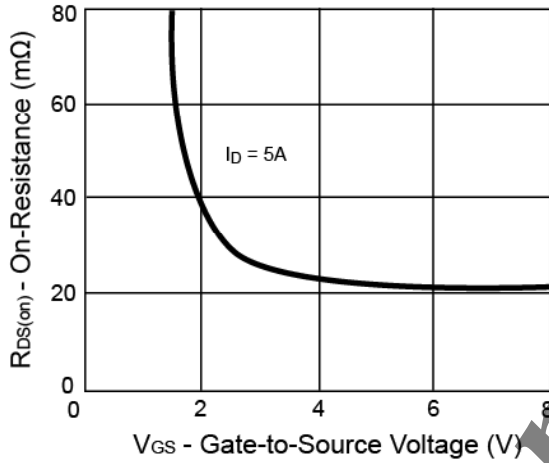
Source-Drain Diode Forward Voltage



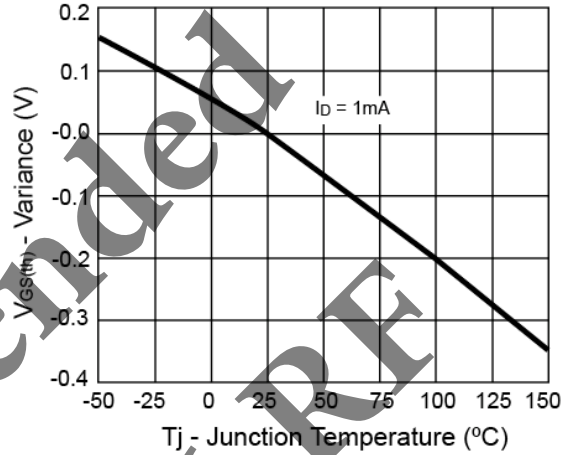


Electrical Characteristics Curve ($T_A = 25^\circ\text{C}$, unless otherwise noted)

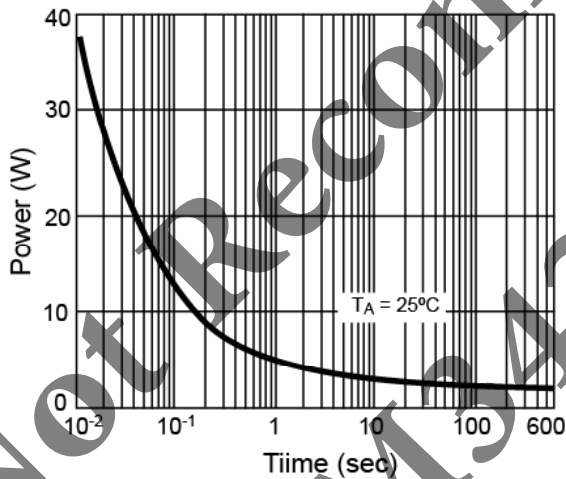
On-Resistance vs. Gate-Source Voltage



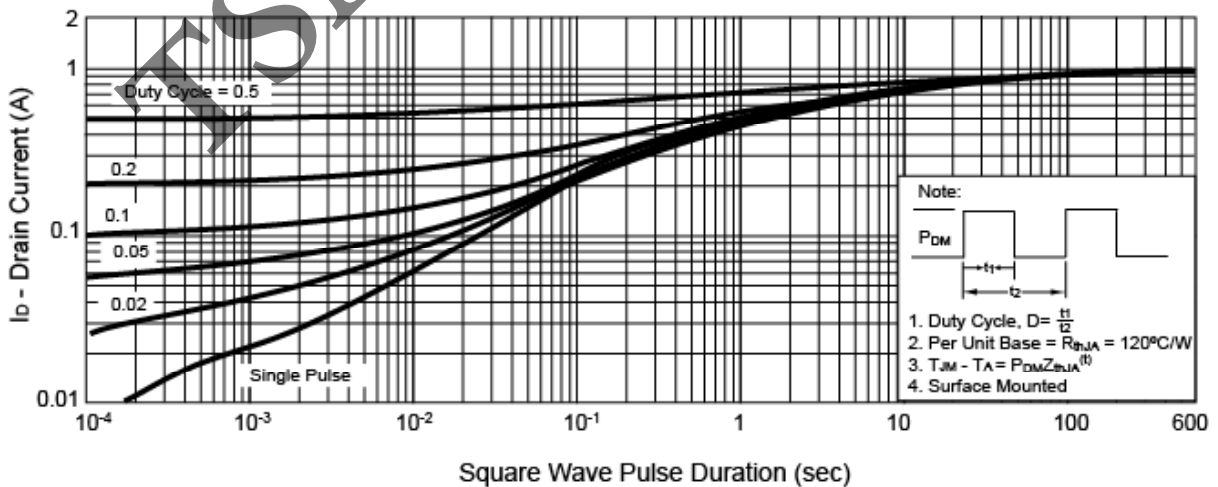
Threshold Voltage



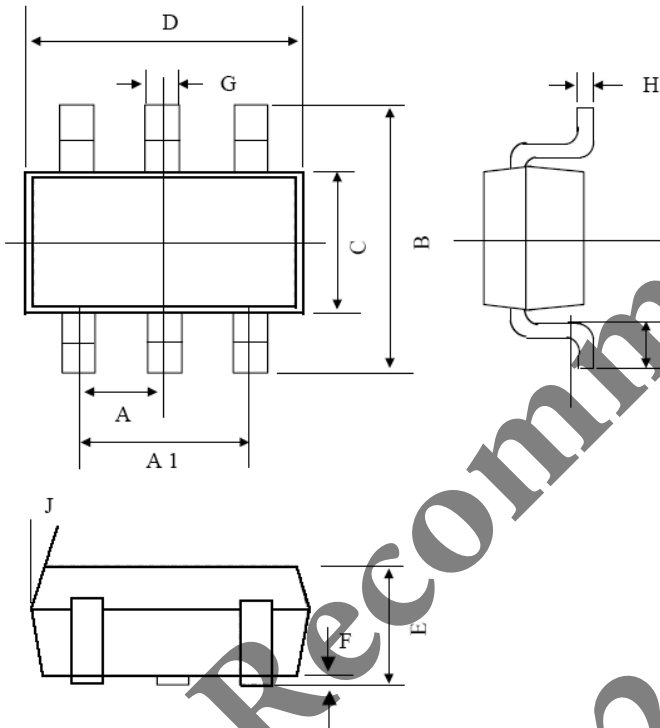
Single Pulse Power



Normalized Thermal Transient Impedance, Junction-to-Ambient

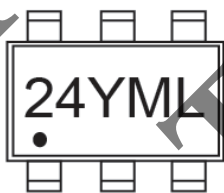


SOT-26 Mechanical Drawing



SOT-26 DIMENSION						
DIM	MILLIMETERS			INCHES		
	MIN	TYP	MAX	MIN	TYP	MAX
A	0.95 BSC			0.0374 BSC		
A1	1.9 BSC			0.0748 BSC		
B	2.60	2.80	3.00	0.1024	0.1102	0.1181
C	1.40	1.50	1.70	0.0551	0.0591	0.0669
D	2.80	2.90	3.10	0.1101	0.1142	0.1220
E	1.00	1.10	1.20	0.0394	0.0433	0.0472
F	0.00	--	0.10	0.00	--	0.0039
G	0.35	0.40	0.50	0.0138	0.0157	0.0197
H	0.10	0.15	0.20	0.0039	0.0059	0.0079
I	0.30	--	0.60	0.0118	--	0.0236
J	5°	--	10°	5°	--	10°

Marking Diagram



- 24** = Device Code
- Y** = Year Code
- M** = Month Code
- (**A**=Jan, **B**=Feb, **C**=Mar, **D**=Apr, **E**=May, **F**=Jun, **G**=Jul, **H**=Aug, **I**=Sep, **J**=Oct, **K**=Nov, **L**=Dec)
- = Month Code for Halogen Free Product
- (**O**=Jan, **P**=Feb, **Q**=Mar, **R**=Apr, **S**=May, **T**=Jun, **U**=Jul, **V**=Aug, **W**=Sep, **X**=Oct, **Y**=Nov, **Z**=Dec)
- L** = Lot Code

Not Recommended
TSM3424CX6 RF

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